

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## Mail Stop Patent Application

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

22386 U.S. PTO  
10/701249

Transmitted herewith for filing under 37 CFR 1.53(b) are the specification and claims of the nonprovisional patent application of:

Junji ICHIMIYA

Inventor(s)

LAYOUT DESIGN METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUIT, AND SEMICONDUCTOR INTEGRATED CIRCUIT

Title of Invention

## APPLICATION ELEMENTS ENCLOSED:

1. X Specification, total pages 37 including:
  - a. 6 pages of claims (12 claims)
  - b. 1 page(s) Abstract
2. X 18 sheets of      informal X formal drawings (Figs. 1-20)
3.      Oath or declaration of Applicant(s) (      total pages)
  - a.      Newly executed (original)
  - b.      Unexecuted
  - c.      Copy from a prior application (37 CFR 1.63(d))  
(for continuation/divisional with Item 14 completed)
4.      Deletion of Inventor(s)  
Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b)
5.      Incorporation By Reference  
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied in item 3b is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6.      Microfiche Computer Program (Appendix)

## APPLICATION PARTS ENCLOSED:

7.      An assignment (cover sheet & document(s))
8.      37 CFR 3.73(b) Statement (*when there is an assignee*)
9.      Power of Attorney
  - a.      Newly executed (original)
  - b.      Unexecuted
  - c.      Copy from a prior application

10. X Information Disclosure Statement (IDS)/PTO-1449

X Copies of IDS Citations

11.     A Preliminary Amendment

12. X Return Receipt Postcard (MPEP 503)

13.     Applicant(s) hereby establish small entity status under 37 C.F.R. §1.9 and §1.27

14.     If a Continuing Application, *check the appropriate box and supply the requisite information:*

☐ Continuation of prior application No.: \_\_\_\_\_ filed \_\_\_\_\_

☐ Divisional of prior application No.: \_\_\_\_\_ filed \_\_\_\_\_

☐ Continuation-in-part (CIP) of prior application No.: \_\_\_\_\_ filed \_\_\_\_\_

15.     Amend the specification by inserting before the first line of page one: --This is a     continuation  
\_\_\_\_\_ divisional of application Serial No. \_\_\_\_\_ filed \_\_\_\_\_--

16. X Priority of application No. 2002-320912 filed in Japan on November 5, 2002. Applicant hereby claims  
priority under 35 U.S.C. §119.

X A certified copy of priority application No. 2002-320912 is enclosed herewith:

    A certified copy of priority application No. \_\_\_\_\_ has been filed in prior application  
S.N. \_\_\_\_\_ filed \_\_\_\_\_

17.     Other (identify) \_\_\_\_\_

The filing fee is calculated as follows:

**CLAIMS AS FILED, LESS ANY CLAIMS CANCELLED BY AMENDMENT**

	Number Filed		Number Extra*		Rate			Fee	
					Small Entity	Other Entity		Small Entity	Other Entity
Total Claims	12 - 20	=	0	X	\$9	\$18	=	\$ 0	\$0
Indep. Claims	2 - 3	=	0	X	\$43	\$86	=	\$ 0	\$0
Multiple Dependent Claims Presented: Yes <u>   </u> No <u>X</u>					\$145	\$290	=	\$ 0	\$0
If the difference in column 1 is less than zero, enter "0" in column 2					Basic Fee			\$385.00	\$770.00
					Total Fee			\$	\$770.00

18. X A check in the amount of \$770.00 to cover the filing fee.
19.     A check in the amount of \$        for recordation of the Assignment.
20. X The Commissioner is hereby authorized to charge any additional fees which may be required in connection with the following or credit any overpayment to Deposit Account No. 03-3125.
- X Filing fees under 37 C.F.R. §1.16.
- X Patent application processing fees under 37 C.F.R. §1.17.
- The issue fee set in 37 C.F.R. §1.18 at or before mailing of the Notice of Allowance, pursuant to 37 C.F.R. §1.311(b).
21. X Three copies of this sheet are enclosed.
22.     Other (identify) \_\_\_\_\_

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Respectfully submitted,

  
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November 4, 2003  
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Junji ICHIMIYA  
Serial No. : Not Yet Known  
Date Filed : Herewith  
For : LAYOUT DESIGN METHOD FOR SEMICONDUCTOR  
INTEGRATED CIRCUIT, AND SEMICONDUCTOR INTEGRATED  
CIRCUIT

**Mail Stop Patent Application**

Commissioner for Patents  
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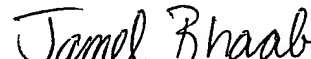
Sir:

**EXPRESS MAIL LETTER OF TRANSMITTAL**

Express Mail mailing label number: EV325706628US

Date of Deposit: November 4, 2003

I hereby certify that the above-identified application of Junji ICHIMIYA, filed herewith, including Specification (37 pages), Claims (1-12, 6 pages), Abstract (1 page), 18 sheets of formal drawings (Figs. 1-20), Information Disclosure Statement (including Form PTO-1449 and copy of reference), certified copy of Japanese Patent Application No. 2002-320912, application transmittal in triplicate, and a check in the amount of \$770.00 for filing fee, is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and is addressed to the Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.

  
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Jamel Rhaab

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Depositing Express Mail Material

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